

**IN THE CLAIMS:**

Please cancel claims 1-36 and add new claims 37-40, as set forth below.

Claims 1-36 (Canceled)

1           37.     (New) An apparatus comprising:  
2     a first register to provide an enable/disable bit for a thermal management system on an  
3         integrated circuit die;  
4     a second register to selectively disengage a specified portion of the thermal management  
5         system;  
6     a third register to enable the thermal management system in response to an external  
7         event;  
8     a fourth register to force the thermal management system active while overriding a  
9         disable bit provided by the first register; and  
10    a fifth register to allow external software and hardware to enable the thermal  
11         management system.

1           38.     (New) The apparatus of claim 37, further comprising a visibility element  
2     to indicate a status of an output of a temperature sensor associated with the thermal  
3     management system.

1           39.     (New) The apparatus of claim 38, the visibility element comprising:  
2     a register to indicate the status of the temperature sensor output;  
3     another register to provide a sticky bit;  
4     a counter to count a number of lost clock cycles resulting from operation of the thermal  
5         management system; and  
6     circuitry to generate an interrupt when the temperature sensor output transitions to a  
7         different state.

1           40.     (New) The apparatus of claim 38, further comprising a power modulation  
2     element to reduce power consumption of the integrated circuit die in response to the  
3     temperature sensor output.